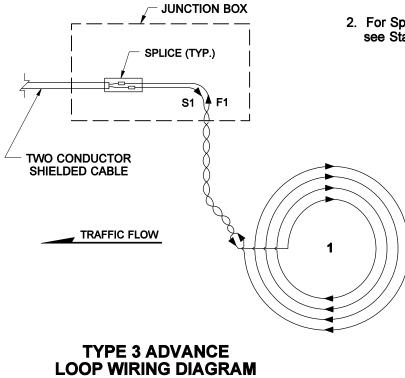
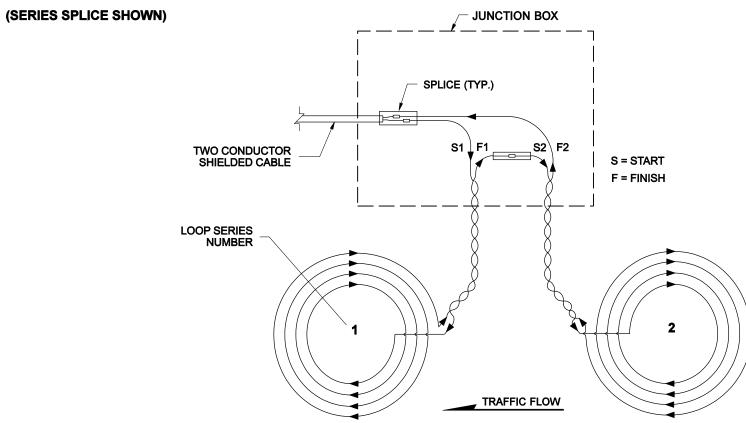


NOTES

- All of the loop lead-in wires shall return to the Junction Box.
- 2. For Splice Detail, see Standard Plan J-8d.





TYPE 3 SAMPLING LOOP WIRING DIAGRAM
(SERIES SPLICE SHOWN)



TYPE 3 INDUCTION LOOP

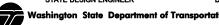
STANDARD PLAN J-8c

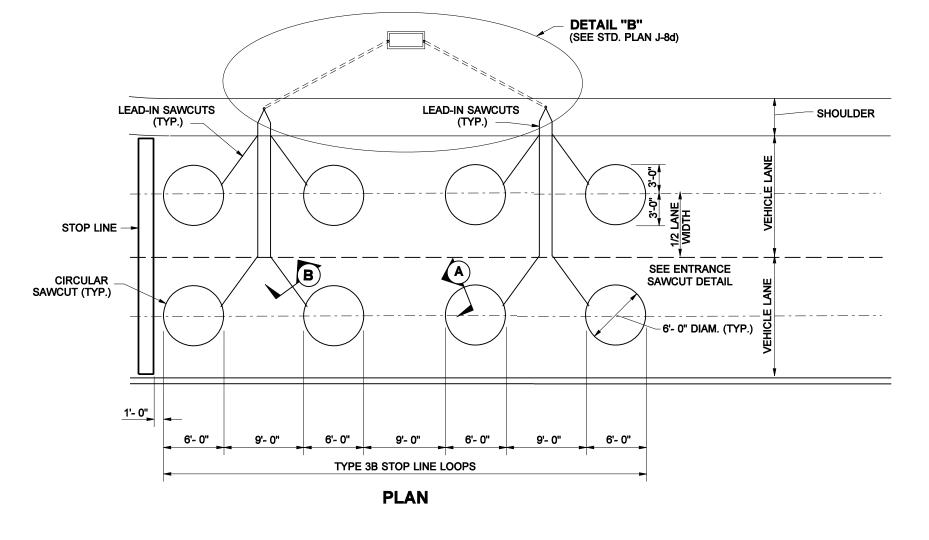
APPROVED FOR PUBLICATION

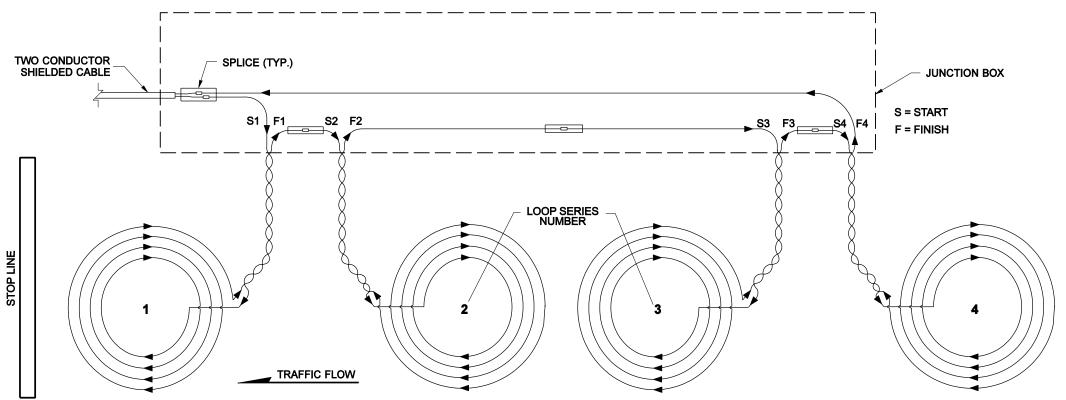
SHEET 2 OF 3 SHEETS

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TYPE 3B STOP LINE LOOP WIRING DIAGRAM (SERIES SPLICE SHOWN)



TYPE 3 INDUCTION LOOP

STANDARD PLAN J-8c

SHEET 3 OF 3 SHEETS

APPROVED FOR PUBLICATION 05-20-04



